

PATENT APPLICATION**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Assistant Commissioner for Patents
Washington, DC 20231

REVOCATION AND NEW POWER OF ATTORNEY BY ASSIGNEE

Skyworks Solutions, Inc., owner of the entire right, title and interest in, to and under the applications and patents listed in the attached Schedule A by virtual of assignments, hereby revokes any and all former powers of attorney and appoints the attorneys and agents associated with Customer Number



and Daniel N. Yannuzzi, Reg. No. 36,727, and Stephen Warhola, Reg. No. 43,237, of Skyworks Solutions, Inc., 5221 California Avenue, Mail Stop 41-1C, Irvine, CA 92612, as Assignee's attorneys with full power of substitution and revocation to take any and all action necessary with regard to the patent applications and patents listed in the attached Schedule A.

Address all telephone calls to Rick A. Toering at telephone number 703-464-4806.

Address all correspondence to Customer Number



ASSIGNEE:

Sk,works Solutions, Inc.

The undersigned hereby declares that all statements made herein of his/her own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SKYWORKS SOLUTIONS, INC.By: Name: STEPHAN WARHOLATitle: Chief IP CounselDate: 1/28/03

SCHEDULE A

REFERENCE NO.	INVENTION	DATE	INVENTOR	FILED
26169-100	SYSTEM AND METHOD FOR COMMUNICATION PARAMETER DETERMINATION	09/15/7209	18-Sep-1998	2141
26169-101	METHOD AND APPARATUS FOR EXTENDING A VCO TUNING RANGE	09/498378	03-Feb-2000	6,204,734 20-Mar-2001
26169-102	GMC FILTER AND METHOD FOR SUPPRESSING UNWANTED SIGNALS INTRODUCED BY THE FILTER	09/663848	18-Sep-2000	2816
26169-103	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	09/506302	17-Feb-2000	
26169-104	SEQUENTIAL DC OFFSET CORRECTION FOR AMPLIFIER CHAIN	10/1170507	13-Jun-2002	9500
26169-105	SYSTEM FOR CANCELLING INTERNAL INTERFERENCE IN A RECEIVER	09/495993	01-Feb-2000	6,259,752 10-Jul-2001
26169-106	DIFFERENTIAL OSCILLATOR	09/675239	29-Sep-2000	3259
26169-107	SWITCHABLE GAIN AMPLIFIER	10/1171891	14-Jun-2002	2107
26169-108	METHOD AND APPARATUS FOR ESTABLISHING COMPATIBILITY BETWEEN CORDLESS TELEPHONE APPLICATIONS AND PHYSICAL HARDWARE OF THE CORDLESS TELEPHONE	09/479127	07-Jan-2000	
26169-109	SYSTEM FOR EIGHT-PHASE 45 POLYPHASE FILTER WITH AMPLITUDE MATCHING	09/666501	18-Sep-2000	6,388,543 14-May-2002
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INVENTION NUMBER	INVENTION TITLE	INVENTOR'S NAME	DATE OF INVENTION	DATE OF FILING	DATE OF PUBLICATION	PATENT NUMBER
26169-110	SUBHARMONIC MIXER CIRCUIT AND METHOD	09/1688679	25-Sep-2000	6,370,372	09-Apr-2002	2776
26169-111	DUAL MODE PHONE ARCHITECTURE UTILIZING A SINGLE TRANSMIT-RECEIVE SWITCH	09/013235	26-Jan-1998			
26169-112	SYSTEM FOR CANCELLING INTERNAL INTERFERENCE IN A RECEIVER	09/634081	08-Aug-2000			
26169-113	VCO TUNING CURVE COMPENSATED CHARGE PUMP CURRENT SYNTHESIZER	09/675889	29-Sep-2000			
26169-114	VCO TUNING CURVE COMPENSATED CHARGE PUMP CURRENT SYNTHESIZER	09/742256	20-Dec-2000		8067	
26169-116	DUAL-TUNE INPUT INTEGRATED VCO ON A CHIP	09/706921	06-Nov-2000			
26169-117	LONGITUDINALLY-COUPLED SAW FILTER WITH ENHANCED FREQUENCY SELECTIVITY	09/545232	07-Apr-2000	6,417,746	09-Jul-2002	
26169-118	TIMING ESTIMATION IN MOBILE COMMUNICATION SYSTEMS USING PARABOLIC INTERPOLATOR	09/163640	30-Sep-1998	6,219,345	17-Apr-2001	
26169-119	METHOD OF ESTIMATING CARRIER FREQUENCY IN A DIGITAL MOBILE COMMUNICATIONS SYSTEM THROUGH MULTIPLE HYPOTHESES PERFORMED ON THE RECEIVED SIGNAL	09/164534	30-Sep-1998			

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Patent Number	Inventor's Name	Title	Filing Date	Pending Date	Issue Date	Expiry Date
26169-120	UNIVERSAL RADIO ARCHITECTURE FOR LOW-TIER PERSONAL COMMUNICATION SYSTEM	081348359	30-Nov-1994	5,648,985	15-Jul-1997	
26169-121	DISTORTION CONTROL FEEDBACK LOOP UTILIZING A NON-LINEAR TRANSFER FUNCTION GENERATOR TO COMPENSATE FOR NON-LINEARITIES IN A TRANSMITTER CIRCUIT	09144404	31-Aug-1998	6,321,072	20-Nov-2001	
26169-122	SYSTEM AND METHOD FOR PROVIDING A TRAP AND PATCH FUNCTION TO LOW POWER, COST CONSCIOUS, AND SPACE CONSTRAINED APPLICATIONS	091232881	15-Jan-1999	6,412,081	25-Jun-2002	
26169-123	APPARATUS AND METHOD FOR IMPROVING POWER CONTROL LOOP LINEARITY	091161014	25-Sep-1998	6,466,772	15-Oct-2002	
26169-124	POWER AMPLIFIER DRIVER SYSTEM FOR WIRELESS HANDSET	091328927	09-Jun-1999	6,339,361	15-Jun-2002	
26169-125	PROGRAMMABLE FREQUENCY DIVIDER	091370099	06-Aug-1999			
26169-126	DIRECT CONVERSION RECEIVER MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER	091260919	02-Mar-1999	6,360,087	19-May-2002	
26169-127	PREPROCESSOR AND RELATED FREQUENCY TRANSLATOR	091386865	31-Aug-1999			
26169-128		091261056	02-Mar-1999	6,393,266	21-May-2002	

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Case Number	Inventor's Name	Attalaentor's Name	Priority Date	Issue Date	Serial No.
26169-129	DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND RELATED PREPROCESSOR	09/386956	27-Aug-1999	1958	
26169-130	SYSTEM OF AND METHOD FOR COMPENSATING A BASEBAND SIGNAL TO REDUCE THIRD ORDER MODULATION DISTORTION	09/515538	29-Feb-2000	1027	
26169-131	A BALUN CIRCUIT FOR COMBINING DIFFERENTIAL POWER AMPLIFIER OUTPUTS	09/386863	31-Aug-1999		
26169-132	SYSTEM OF AND METHOD FOR REDUCING OR ELIMINATING THE UNWANTED SIDEBAND IN THE OUTPUT OF A TRANSMITTER COMPRISING A QUADRATURE MODULATOR FOLLOWED BY A TRANSLATIONAL LOOP	09/514501	29-Feb-2000		
26169-133	DOHERTY POWER AMPLIFIER WITH INTEGRATED QUARTER WAVE TRANSFORMER/COMBINER CIRCUIT	09/761100	16-Jan-2001	3670	
26169-134	SYSTEM OF AND METHOD FOR REDUCING OR ELIMINATING THE UNWANTED SIDEBAND IN A SIGNAL DERIVED FROM THE OUTPUT OF A QUADRATURE MODULATOR	09/515633	29-Feb-2000	9685	

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Patent Number	Inventor's Name	Priority Date	Original Filing Date	Grant Date	Classification	Examiner	Office
26169-135	POWER AMPLIFIER SATURATION DETECTION AND COMPENSATION	09/651801	30-Aug-2000				2087
26169-136	ADAPTIVE CONFIGURABLE DEMODULATION SYSTEM WITH MULTIPLE OPERATING MODES	09/710764	08-Nov-2000				
26169-137	Digital Phase/Frequency Detector DIRECT SEQUENCE SPREAD SPECTRUM SYSTEM WITH ENHANCED FORWARD ERROR CORRECTION CODE FEATURE	09/739822	05-Mar-2001				4820
26169-138	BURST DETECTOR ZERO-OVERHEAD SYMBOL RATE ADAPTATION SYSTEM FOR QVSF CODE	09/735369	12-Dec-2000				3929
26169-139	DYNAMIC RANGE REDUCTION CIRCUITRY FOR A DIGITAL COMMUNICATIONS RECEIVER	09/718985	22-Nov-2000				1563
26169-140	LOW VOLTAGE DUAL-MODULUS PRESCALER CIRCUIT USING MERGED PSEUDO-DIFFERENTIAL LOGIC	09/159328	23-Sep-1994	6,445,732	03-Sep-2002		
26169-141	SOURCE-SWITCHED OR GATE-SWITCHED CHARGE PUMP HAVING CASCODED OUTPUT PROGRAMMABLE RELAXATION OSCILLATOR GATED DELAY-LOCKED LOOP FOR CLOCK GENERATION APPLICATIONS	09/163962	30-Sep-1998	6,157,693	05-Dec-2000		
26169-142							
26169-143							
26169-144							
26169-145							

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Patent Number	Inventor Name	Serial Number	Issue Date	Examiner	Filing Date	Priority Date	Publication Date
26169-146	PHASE-LOCKED LOOP HAVING TEMPERATURE-COMPENSATED BANDWIDTH CONTROL	09/314898	19-May-1999	6,211,743	03-Apr-2001		
26169-147	PEER-TO-PEER DATA TRANSFER USING PRE-EXISTING CALLER ID CLASS FSK SIGNALING INFRASTRUCTURE	09/328048	08-Jun-1999	6,304,642	16-Oct-2001		
26169-148	MULTI-BAND FILTER SYSTEM FOR WIRELESS COMMUNICATION RECEIVER	09/330646	11-Jun-1999	6,466,768	15-Oct-2002		
26169-149	PEER-TO-PEER TRANSFER USING PRE-EXISTING CALLER ID CLASS FSK SIGNALING INFRASTRUCTURE	09/363989	16-Jul-1999	6,404,868	11-Jun-2002		
26169-150	MULTI-BAND TRANSCEIVER HAVING MULTI-SLOT CAPABILITY	09/387038	31-Aug-1999				
26169-151	MULTI-BAND TRANSCEIVER HAVING MULTI-SLOT CAPABILITY	09/392355	08-Sep-1999				
26169-151-401	MULTI-BAND RECEIVER HAVING MULTI-SLOT CAPABILITY	10/277516	21-Oct-2002				
26169-152	WIRELESS TRANSMITTER HAVING A MODIFIED TRANSLATION LOOP ARCHITECTURE	09/398911	14-Sep-1999				